7 N	772	Search Text	DB	Time stamp
L Number	Hits 212136		USPAT	2003/08/21 15:26
2	955104		USPAT	2003/08/21 15:26
5	1028736		USPAT	2003/08/21 15:28
6	2091587	l 1777 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	USPAT	2003/08/21 15:28
7	383225		USPAT	2003/08/21 15:29
8	875713		USPAT	2003/08/21 15:30
9	2127209	predetermined number\$1	USPAT	2003/08/21 15:30
10	2021085	1 *	USPAT	2003/08/21 15:31
11	644272	· ·	USPAT	2003/08/21 15:33
12	140224	(logical comparator) and (failure analysis	USPAT	2003/08/21 15:34
12	140224	memory)	OSFAI	2003/08/21 13:34
13	114663	((logical comparator) and (failure analysis memory)) and (bad block detection)	USPAT	2003/08/21 15:35
14	108446	(((logical comparator) and (failure analysis memory)) and (bad block detection)) and (bad address line detection)	USPAT	2003/08/21 15:36
15	45726	·	USPAT	2003/08/21 15:37
16	41569		USPAT	2003/08/21 15:39
17	40740	((((((logical comparator) and (failure analysis memory)) and (bad block detection) and (bad address line detection)) and (mask controller\$1)) and (failure memory	USPAT	2003/08/21 15:40
19	39424	<pre>cell\$1)) and (predetermined number\$1) (((((((logical comparator) and (failure analysis memory)) and (bad block detection)) and (bad address line detection)) and (mask controller\$1)) and (failure memory cell\$1)) and (predetermined number\$1)) and (address line\$1)</pre>	USPAT	2003/08/21 15:43
20	38423	<pre>((((((((logical comparator) and (failure analysis memory)) and (bad block detection)) and (bad address line detection)) and (mask controller\$1)) and (failure memory cell\$1)) and (predetermined number\$1)) and (address line\$1)) and (bad memory detection)</pre>	USPAT	2003/08/21 15:44
21	210965	comparing and (written logical value with read-out logical value)	USPAT	2003/08/21 15:44
22	16236	<pre>((((((((((((((((((((((((((((((((((((</pre>	USPAT	2003/08/21 15:46
23	1859	test\$1)	USPAT	2003/08/21 15:48
24	121	<pre>((((((((((((((((((((((((((((((((((((</pre>	USPAT	2003/08/21 15:49
25	348875	(replacing or substituting or changing) and (failure memory cell\$1 with spare block\$1 or redundant block\$1)	USPAT	2003/08/21 15:51
27	2458	714/718.ccls. or 714/719.ccls. or 714/710.ccls. or 714/704.ccls. or 714/736.ccls.	USPAT	2003/08/21 15:53

1 ((((((((((((((((((((((((((((((((((((2003/08/21 15:53
) and (bad address line detection)) and (mask controller\$1)) and (failure memory cell\$1)) and (predetermined number\$1)) and	
(mask controller\$1)) and (failure memory cell\$1)) and (predetermined number\$1)) and	
cell\$1)) and (predetermined number\$1)) and	
(address line\$1)) and (bad memory	
detection)) and (comparing and (written	
logical value with read-out logical value)))	
and (discontinuing and (failue memory cell\$1	
with test\$1))) and ((replacing or	
substituting or changing) and (failure	
memory cell\$1 with spare block\$1 or	
redundant block\$1))) and (714/718.ccls. or	
714/719.ccls. or 714/710.ccls. or	
714/704.ccls. or 714/736.ccls.)	
26 83 (((((((((((((((((((((((((((((((((((2003/08/21 15:58
analysis memory)) and (bad block detection)	
) and (bad address line detection)) and	
(mask controller\$1)) and (failure memory	
cell\$1)) and (predetermined number\$1)) and	
(address line\$1)) and (bad memory	
detection)) and (comparing and (written	
logical value with read-out logical value)))	
and (discontinuing and (failue memory cell\$1	
with test\$1))) and ((replacing or	1
substituting or changing) and (failure	
memory cell\$1 with spare block\$1 or	1
redundant block\$1))	1